# EE4280 Lecture 1: **Nonlinearity**

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## **Nonlinearity**

Nonlinear characteristic deviates from a straight line as the input swing increases





For small input swing, the output is a reasonable replica of the input

• Small-signal gain is related to the slope at a given bias point

# **Nonlinearity**

Nonlinear characteristic deviates from a straight line as the input swing increases Waveformdistortion



- For small input swing, the output is a reasonable replica of the input
- For large input swings, most amplifiers experience gain compression (instead of expansion)
	- The output exhibits "saturated" levels due to supply voltage or bias current

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## Nonlinearity

Can be viewed as variation of the slope (small-signal gain) with the input level (common-mode)



## To Quantify Nonlinearity (I)



## **To Quantify Nonlinearity (II)**

#### A single tone test

 $\Delta x = A \cos \omega_1 t$   $\chi(t) = \chi_0 + A \cos \omega_1 t$  $\Delta y = \alpha_1 A \cos \omega_1 t + \alpha_2 A^2 \cos^2 \omega_1 t + \alpha_3 A^3 \cos^3 \omega_1 t + ...$  $\frac{\alpha_{3} A^{2}}{2} (1 + \omega_{3} 2 \omega_{1} t)$   $\alpha_{3} A^{3} (\frac{3}{4} \omega_{3} \omega_{1} t + \frac{1}{4} \omega_{3} 3 \omega_{1} t)$ =  $(a_1 A + \frac{3}{4} a_3 A^3) cos \omega_1 t$ <br>+  $\left(\frac{a_2 A^3}{2}\right) cos 2\omega_1 t$  $\mathbf{M}$  $+\frac{1}{4}$  as  $\Lambda^3$  cas 3  $\mu_1$ t +...  $\sim \propto \frac{a^{2}}{a} A^{2}$ Total harmonic distortion  $\sqrt{\frac{1}{4}a_3A^3}$  $rac{a_{2}}{a_{1}}$  $THD =$  $\left(\alpha_1 A+\frac{3}{4}\alpha_3A^3\right)^2$ 

![](_page_7_Figure_0.jpeg)

![](_page_8_Figure_0.jpeg)

## **Nonlinearity of Differential Circuits (I)**

Differential circuits exhibit an "odd-symmetric" input/output **characteristics, i.e.,**  $f(-x) = -f(x)$ 

![](_page_9_Figure_2.jpeg)

#### $\delta Var = f(\delta V)$

For the Taylor expansion to be an odd function, all the even-order  $V = f(x)$ terms musts be zero.  $+$  if fer ential operation:  $f(-x) = -f(x)$ 

$$
\Delta y = \alpha_1 \Delta x + \alpha_3 (\Delta x)^3 + \alpha_5 (\Delta x)^5 + \dots
$$

A differential circuit produces no even-order harmonics

### Nonlinearity in Differential Circuits (II)

![](_page_10_Figure_1.jpeg)

Single-ended and differential amplifiers with the same voltage gain

• Single-ended and differential amplifiers with the same voltage gain  
\n
$$
|A_v| \approx g_m R_D
$$
\n
$$
= \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{TH}) R_D
$$
\n• For the single-ended case: 
$$
V_{00} - V_{\text{PW1}}(t) = R_0 \left(\frac{1}{2} M \cos \frac{W}{L} (V_{int}(t) - V_{th})^2\right)
$$
\n
$$
V_{DD} - V_{out} = I_D \cdot R_D
$$
\nwith 
$$
V_m = V_{GS} + V_m \cos \omega t
$$
\nWe have 
$$
V_{DD} - V_{out} = \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GS} + V_m \cos \omega t - V_m)^2 R_D
$$
\n**EXECUTE:**

## **Nonlinearity for Differential Circuits (III)**

• For the single-ended case:  
\n
$$
\begin{array}{rcl}\n\sqrt[3]{2}V_{\text{out}} & = & \frac{V_{00}}{2}V_{\text{in}} & \frac{V_{\text{in}}}{2}V_{\text{out}} & \frac{V_{\text{in}}}{2}V_{\text{out}} \\
V_{\text{out}} = & \frac{1}{2}\mu_{\text{in}}C_{\text{out}}\frac{V_{\text{in}}V_{\text{in}} + V_{\text{in}}\cos(\omega t - V_{\text{in}})^{2}R_{\text{in}}}{V_{\text{in}} - V_{\text{in}}}\n\end{array}
$$
\n
$$
= f(V_{\text{in}}) + \frac{f'(V_{\text{0}}S_{\text{in}})}{1}
$$
\n
$$
= 1.1V + f'(V_{\text{0}}S_{\text{in}}) \cdot V_{\text{in}} \cdot \cos(\omega t + \frac{1}{2}) \cdot f''(V_{\text{0}}S_{\text{in}}) (V_{\text{in}} - V_{\text{0}}S_{\text{in}})^{2}
$$
\n
$$
= 1.1V + f'(V_{\text{0}}S_{\text{in}}) \cdot V_{\text{in}} \cdot \cos(\omega t + \frac{1}{2}) \cdot f''(V_{\text{0}}S_{\text{in}}) (V_{\text{in}} - V_{\text{0}}S_{\text{in}})^{2}
$$
\n
$$
= 1.1V + f'(V_{\text{0}}S_{\text{in}}) \cdot V_{\text{in}} \cdot \cos(\omega t + \frac{1}{2}) \cdot f''(V_{\text{0}}S_{\text{in}}) (V_{\text{in}} - V_{\text{0}}S_{\text{in}}) \cdot V_{\text{0}}}{V_{\text{0}} + V_{\text{0}}S_{\text{in}} \cdot V_{\text{0}} \cdot V_{\text{0}}}\n\end{array}
$$
\n
$$
\begin{array}{rcl}\n\frac{V_{\text{in}}}{V_{\text{in}}} & = & \frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{\text{in}}}{V_{\text{in}}}\frac{V_{
$$

#### Nonlinearity for Differential Circuits (IV)

![](_page_12_Figure_1.jpeg)

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## Effect of Negative Feedback on Nonlinearity (I)

- Negative feedback makes the closed-loop gain relatively independent of the op amp's open-loop gain  $\rightarrow$  Gain Desensitization
- Nonlinearity can be viewed as small-signal gain variation with input level  $\rightarrow$  suppressed by negative feedback as well

![](_page_13_Figure_3.jpeg)

## Effect of Negative Feedback on Nonlinearity (II)

$$
\text{With } \left\{\n\begin{array}{l}\na = (\alpha_1 - \frac{\lambda_1 \lambda_2 \lambda_3 \lambda_4}{\lambda_4})(V_m - \beta a) & \text{if } a \in \mathbb{R}^2, \\
b = -\alpha_1 \beta b + \frac{\alpha_2 (V_m - \beta a)^2}{2}\n\end{array}\n\right\}
$$
\n
$$
\text{for } a \in \mathbb{R}^2, \text{if } a \in \mathbb{R}^2
$$

$$
y = \frac{1}{\sqrt{1-\frac{1}{1\sqrt{1+\frac{1}{1\sqrt{1+\frac{1}{1\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{1+\frac{1}{1\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{11\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{11\sqrt{11\{1\frac{1}{\sqrt{11\{1\frac{1}{\sqrt{11\{1\frac{1}{\sqrt{11\{1\{1\frac{1}{\sqrt{11\{1\{1\{1\{1\frac{1}{\sqrt{11\{1\{1\{1\{1\{1\{1\{1\{1\{1\{1\{1\{
$$

- The second harmonic distortion:  $\frac{A_{HD2}}{A_F} = \frac{b}{a} = \frac{\alpha_2 V_m}{2 \alpha_1 (1 + \alpha_1 \beta)^2}$
- Compared to the open-loop case:  $\frac{b}{c} = \frac{d \cdot V_m}{c}$  $2\alpha$ With the same input swing:  $FB$  stucture provides an improv. of  $(1 + \alpha + \beta)^2$

With the same output swing:<br>So you need (1<sup>+</sup> di B) x Imger input for the FB Structure -> improv. of  $(1+\alpha_1)$ EE 4280  $\sim$  15  $\sim$ 

![](_page_15_Figure_0.jpeg)

![](_page_16_Figure_0.jpeg)

## Linearization Technique (III)

#### Post correction

• A common-source amplifier is in fact a voltage-to-current converter followed by a current-to-voltage converter so that

![](_page_17_Figure_3.jpeg)

![](_page_18_Figure_0.jpeg)