電路學(10410EE221002)第二次期中考

2015年12月09日 時間:2小時 Close Book

學號	:	
姓名	:	

- There are 12 pages in this midterm exam, including this cover page. Please check that you have them all.
- Please write your 學號 姓名 in the space provided above.
- **IMPORTANT:** The problems in this exam vary in difficulty; moreover, questions of different levels of difficulty are distributed throughout the exam. If you find yourself spending a long time on a question, consider moving on to later problems in the exam, and then working on the challenging problems after you have finished all of the easier ones.
- Do your work and enter your answer for each question within the boundaries of that question. You may do your work on the back of the preceding page. Give a brief explanation if you are asked to explain.
- Remember to include the sign and units for all numerical answers.
- This is a closed-book exam, but you may use a calculator.
- You have 2 hours to complete this exam.
- Good luck!

Table of grades:

1.	2.	3.	4.	5.
6.	7.	8.	9.	
Total Grade:				

The following figures show four circuits, labeled "(1)" through "(4)", together with the waveform for the source in each circuit. The figures also show four branch-variable waveforms, labeled "(a)" through "(d)", that could correspond to branch currents i or branch voltages v labeled in circuits. Match the branch variable waveform (a to d) to the appropriate circuit and source waveform (1 to 4). (8%)





2. For the circuit as shown, the switch was opened for a long time before it was closed at time t = 0 and opened at t = 2.5 msec. Find $v_C(t)$ for $t \ge 0$. (12%)





3. For the circuit as shown, find and sketch the zero state response $i_L(t)$ for $t \ge 0$.



(10%)



4. Find the time constant τ of the circuit as shown.







$\tau =$			·	

5. Find the time constant τ of the circuit show.





 $T = \frac{L}{R}$ -R2+R3+R4

6. For the following circuit as shown, assume the initial state of the capacitor $v_c(0)$ is 10V and that of inductor $i_L(0) = 0$ A, answer that following questions. (16%)



(7) Find
$$\frac{dv_c(0^+)}{dt}$$
. (1%)
(8) Sketch the zero-input response $v_c(t)$ for $t \ge 0$. (4%)

(8) Sketch the zero-input response $v_C(t)$ for $t \ge 0$.

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7. Is the zero-input response of the following circuit under-damped, over-damped, or criticallydamped assuming L = 2mH, $C = 0.2\mu$ F, and $R_1 = R_2 = 50 \Omega$? (10%)



α =	5×104	(3%), $\omega_0 =$	5×104	(3%),
Answer:	critically	Clamped		(4%).

- 8. For the RLC circuits as shown, this circuit is under a 125 Hz square wave excitation. The excitation and the response of the capacitator are also shown in the figure below. It is known that the capacitance is equal to $10 \,\mu\text{F}$. From the information given, answer the following questions.
- (a) What is the resistance (approximately) of the resistor that is in the circuit? (6%)
- (b) What is the inductance (approximately) of the inductor that is in the circuit? (6%)



(a) $R = 1.7685$	
(b) $L = 1 + 26 mH$	

9. A signal generator having Norton resistance R_{SG} is connected to Port #1 of a two-port network as shown below. At t = 0, the Norton current $i_{SG}(t)$ of the signal generator takes a step from zero to I_{SG} , and the current $i_O(t)$ is measured at Port #2 as shown below with the port short-circuited. Note that α is a unitless constant satisfying $0 < \alpha < 1$, and τ is a time constant. Assume that the Norton current of the signal generator is zero for a very long time prior to the step.



(a) Which of the following could be the two-port network?



(4%)

(b) Determine the values of *R* and *C* in the network you chose in Part (a). Express the values in terms of R_{SG} , α and τ . (10%)

$$R = \frac{1-\alpha}{\alpha} R_{36}$$

$$C = \frac{T}{R/Rsg}$$