## 電路學(EE2210)第二次期中考

2013年12月9日 時間:2.5小時 Close Book

學號:
姓名:

- There are 12 pages in this midterm exam, including this cover page. Please check that you have them all.
- Please write your 學號 姓名 in the space provided above.
- **IMPORTANT:** The problems in this exam vary in difficulty; moreover, questions of different levels of difficulty are distributed throughout the exam. If you find yourself spending a long time on a question, consider moving on to later problems in the exam, and then working on the challenging problems after you have finished all of the easier ones.
- Do your work and enter your answer for each question within the boundaries of that question. You may do your work on the back of the preceding page.
- Remember to include the sign and units for all numerical answers.
- This is a closed-book exam, but you may use a calculator.
- You have 2.5 hours to complete this exam.
- Good luck!

1.	2.	3.	4.	5.
6.	7.	8.	9.	10.
Total	Grade			

The following figures show four circuits, labeled "(1)" through "(4)", together with the waveform for the source in each circuit. The figures also show four branch-variable waveforms, labeled "(a)" through "(d)", that could correspond to branch currents *i* or branch voltages *v* labeled in circuits. Match the branch variable waveform (a to d) to the appropriate circuit and source waveform (1 to 4).





$(1) \rightarrow \_$	$,(2) \rightarrow \_$	$\underline{},(3) \rightarrow \underline{}$	$\underline{},(4)\rightarrow\underline{}$	·

2. For the circuit as shown, the switch was closed at time t = 0 and opened at t = 1 second. Find  $i_L(t)$  for  $t \ge 0$ . (8%)





3. Find the 1<sup>st</sup> order differential equation for state  $i_{L1}$  in the circuit (as shown in the following figure) in state-variable form (i.e. in terms of  $v_{C1}$ ,  $v_{C2}$ ,  $i_{L1}$ ,  $i_{L2}$ ,  $v_s$  and  $i_s$ ) (8%)



 $L_1 \frac{di_{L1}}{dt} =$ 

4. For the circuit as shown, derive the 2<sup>nd</sup> order differential equation for  $v_C$ . Find the voltage  $v_C(t)$  of the circuit for  $t \ge 0$ , assuming that  $v_C(0^-) = 0$  and  $i_L(0^-) = 0$ . Please express your answer in terms of  $\Lambda$ , *R*, *L*, and *C*. (8%)





5. For the circuit as shown, find and sketch the zero state response  $i_L(t)$  for  $t \ge 0$ .  $v_S$  is a 10V step at t = 0. (8%)





6. Find the time constant  $\tau$  of the circuit as shown.



(8%)

7. Is the zero-input response of the following circuit under-damped, over-damped, or criticallydamped assuming L = 5mH,  $C = 0.5\mu$ F, and  $R_1 = R_2 = 200 \Omega$ ? (8%)



α =	_(3%), $\omega_0 = $	_(3%),
Answer:		_(2%).

8. For the circuit as shown with L = 1mH,  $C = 0.1 \mu$ F and  $R = 200\Omega$ , Find and sketch the zero input response  $v_C(t)$  with  $v_S(t) = 5-5u(t)$  V. (16%)



## Solutions:



9. A signal generator having Norton resistance  $R_{SG}$  is connected to Port #1 of a two-port network as shown below. At t = 0, the Norton current  $i_{SG}(t)$  of the signal generator takes a step from zero to  $I_{SG}$ , and the current  $i_O(t)$  is measured at Port #2 as shown below with the port short-circuited. Note that  $\alpha$  is a unitless constant satisfying  $0 < \alpha < 1$ , and  $\tau$  is a time constant. Assume that the Norton current of the signal generator is zero for a very long time prior to the step.



(8%)

(a) Which of the following could be the two-port network?



(b) Determine the values of *R* and *L* in the network you chose in Part (a). And express the values in terms of  $I_{SG}$ ,  $R_{SG}$ ,  $\alpha$  and  $\tau$ . (8%)

(a) Network (circle one):	(A)	(B)	(C)	(D)	(E)	_(8%).
(b) <i>R</i> =		_(4%), <i>L</i> = _				_(4%).

10. San Chang, a NTHU student working for Professor Ele., has just found a rather strange piece of circuitry in the trash just outside the office of Prof. Sistem. The circuit has three exposed terminals and it has exactly one resistor, one inductor, and one capacitor, but San cannot see how they are connected, because the connections are protected by epoxy. He can read the capacitor label: it says that the capacitor has a capacitance of  $1\mu$ F (*Observation O*<sub>1</sub>). However, the other labels are not readable.

Being a naturally curious NTHU student, San takes the circuit to the lab to see what can be determined about this strange device by experiment. San labels the terminals with the letters "x", "y", and "z" with a marker pen. He then takes several measurements. First, San measures the resistances between the terminals of the mystery circuit with an Ohmmeter, and makes the following observations:

x – y: infinity	(Observation $O_2$ )
y – z: infinity	(Observation O <sub>3</sub> )
z – x: 80 Ω	(Observation O <sub>4</sub> )

Next, San uses a signal generator, which may be modeled as a voltage source, to apply a 1 Volt, 100 Hz, square-wave signal from the x terminal to the y terminal. He uses his oscilloscope to display the voltage from z to y superimposed on the square wave, at the same scale as the square wave, as shown in the figure below (*Observation O*<sub>5</sub>). As you can see, He observes that the waveform from z to y follows the square wave, but there is ringing with a cycle period of about 1 ms.



- (a) Draw the mysterious circuit from above *Observations*. Assume that the inductor and capacitor are ideal. Explain your choice. (6%)
- (b) What is the inductance (approximately) of the inductor that is in the circuit? (6%)

