EE2280 Logic Design

HW3

- 1. Use DeMorgan's theorem to remove the complement outside the braces:
 - (a) ((x+w')'+w'y'z+(x+z)'(x+y))',
 - (b) (x(yz'+y'z)'+wy(y'+x'z))',
 - (c) (x+y)'+z'(x'+z)'.

For each problem, use Verilog to simulate the two logic functions before and after brace removal for function verification.