EE2280 Logic Design

HW₃

- 1. Use DeMorgan's theorem to remove the complement outside the braces:
 - (a) (x(y+z)+y'z(x+w))',
 - (b) (x(y'+y(z+w)))',
 - (c) (xy+y(x+z))'.

For each problem, use Verilog to simulate the two logic functions before and after brace removal for function verification.

EE2280 Logic Design 2