

Lab 12: LCD Display (2)

Objective

- ✓ Implement the timer and stopwatch functions of the electronic clock.

Prerequisite

- ✓ Fundamentals of logic gates.
- ✓ Logic modeling in Verilog HDL.
- ✓ Simple logic development and FSM control
- ✓ LCD control

Experiments

- 1 For the time delay of electronic clock in lab7. Instead using 14-segment displays to show the time, use LCD to present all the functions in lab7.
- 2 (Bonus) Integrate the functions in lab7 and lab8 with LCD display.

TA: _____